


Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Page 1 of 1	Application No.	10/757,000
	Filed	1/13/2004
	First Inventor	Grundy, Kevin P.
	Art Unit	2818
	Examiner	
	Atty. Docket No.	SIPI.P117


U.S. Patent Documents					
Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines
	Number	Kind			
PB	20020087915	A1	Shida et al.	Jun., 2002	
PB	20020083255	A1	Greeff et al.	Jun., 2002	

Examiner Signature		Date Considered	1/24/06
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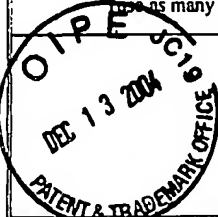
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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PB	6833984		Belgacem	Dec., 2004	

Examiner Signature		Date Considered	11/24/06
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Substitute for Form 1449A/PTO (Modified) (Use as many sheets as necessary)		Attorney Docket No.: SIPI.P117	Application Number: 10/757,000
		First Named Inventor: Grundy, Kevin P.	
		Filing Date: January 13, 2004	Group: 2818

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
PB		5,715,274		Rostoker et al.	02/03/98	
PB		5,860,080		James et al.	01/12/99	
PB		5,945,886		Millar	08/31/99	
PB		6,125,419		Umamura et al.	09/26/00	
PB		6,304,450	B1	Dibene, II et al.	10/16/01	

NON PATENT LITERATURE DOCUMENTS

Exam. Initial*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
PB		IEEE Computer Society, <i>IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)</i> , IEEE Std 1596.4-1996, March 19, 1996, pp. i-91.	
PB		Przybylski, Steven A., <i>New DRAM Technologies</i> , MicroDesign Resources, 1994, pp. 204-215.	
PB		Poulton, John, <i>Signaling in High-Performance Memory Systems</i> , ISSCC 1999, pp. 1-59.	
PB		<i>Serial Network SDRAM</i> presentation, ENEE 759H, Spring 2003, pp. 1-30.	

Examiner Signature	11/24/00 <i>Baron</i>	Date Considered	11/24/00
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